

### SANYO Semiconductors DATA SHEET

## 2SK4101FS — General-Purpose Switching Device Applications

#### **Features**

- · Low ON-resistance, low input capacitance, ultrahigh-speed switching.
- · Adoption of high reliability HVP process.
- · Attachment workability is good by Mica-less package.
- · Avalanche resistance guarantee.

#### **Specifications**

#### Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V <sub>DSS</sub>		650	V
Gate-to-Source Voltage	V <sub>GSS</sub>		±30	V
Drain Current (DC)	I <sub>Dc</sub> *1	Limited only by maximum temperature Tch=150°C	7	Α
	IDpack *2	Tc=25°C (SANYO's ideal heat dissipation condition)*3	6.4	А
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	28	А
Allowable Power Dissipation	Do		2.0	W
	PD	Tc=25°C (SANYO's ideal heat dissipation condition)*3	35	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C
Avalanche Energy (Single Pulse) *4	EAS		194	mJ
Avalanche Current *5	IAV		6	А

Note: \*1 Shows chip capability.

The method is applying silicone grease to the backside of the device and attaching the device to water-cooled radiator made of aluminium.

Marking: K4101

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<sup>\*2</sup> Package limited.

<sup>\*3</sup> SANYO's condition is radiation from backside.

<sup>\*4</sup> VDD=50V, L=10mH, IAV=6A

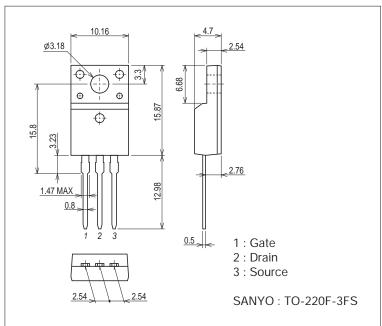
<sup>\*5</sup> L≤10mH, Single pulse

#### Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Linit
			min	typ	max	Unit
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=10mA, VGS=0V	650			V
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =520V, V <sub>GS</sub> =0V			100	μΑ
Gate-to-Source Leakage Current	IGSS	V <sub>GS</sub> =±30V, V <sub>DS</sub> =0V			±100	nA
Cutoff Voltage	V <sub>GS</sub> (off)	V <sub>DS</sub> =10V, I <sub>D</sub> =1mA	3		5	V
Forward Transfer Admittance	yfs	V <sub>DS</sub> =10V, I <sub>D</sub> =3.5A	2.3	4.6		S
Static Drain-to-Source On-State Resistance	R <sub>DS</sub> (on)	I <sub>D</sub> =3.5A, V <sub>GS</sub> =10V		0.85	1.1	Ω
Input Capacitance	Ciss	V <sub>DS</sub> =30V, f=1MHz		750		pF
Output Capacitance	Coss	V <sub>DS</sub> =30V, f=1MHz		136		pF
Reverse Transfer Capacitance	Crss	V <sub>DS</sub> =30V, f=1MHz		28		pF
Turn-ON Delay Time	t <sub>d</sub> (on)	See specified Test Circuit.		21		ns
Rise Time	t <sub>r</sub>	See specified Test Circuit.		40		ns
Turn-OFF Delay Time	t <sub>d</sub> (off)	See specified Test Circuit.		89		ns
Fall Time	tf	See specified Test Circuit.		31		ns
Total Gate Charge	Qg	V <sub>D</sub> S=200V, V <sub>G</sub> S=10V, I <sub>D</sub> =7A		28.5		nC
Gate-to-Source Charge	Qgs	V <sub>DS</sub> =200V, V <sub>GS</sub> =10V, I <sub>D</sub> =7A		5.2		nC
Gate-to-Drain "Miller" Charge	Qgd	V <sub>DS</sub> =200V, V <sub>GS</sub> =10V, I <sub>D</sub> =7A		16		nC
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =7A, V <sub>GS</sub> =0V		0.9	1.2	V

#### **Package Dimensions**

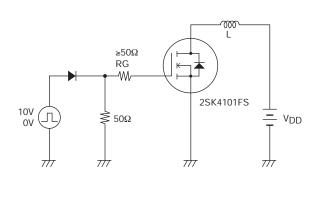
unit : mm (typ) 7528-001

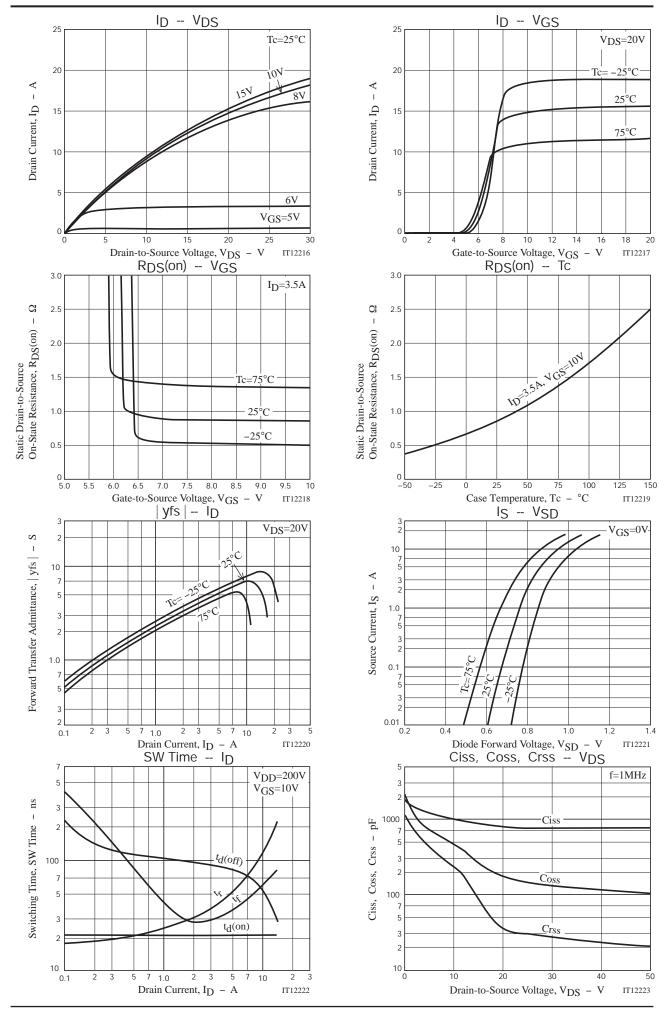


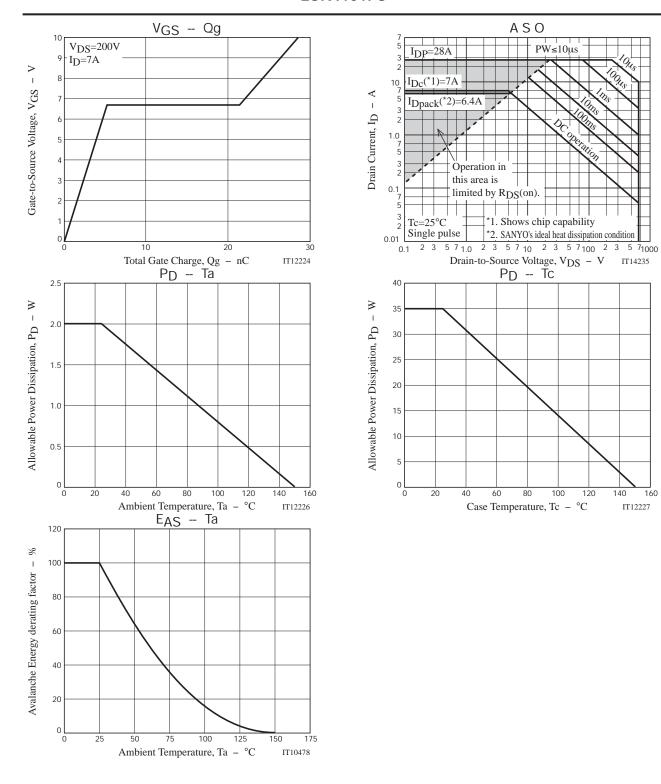
#### **Switching Time Test Circuit**

# VIN VDD=200V 10V 0V ID=3.5A $RL=57\Omega$ VIN D ID=3.5A $RL=57\Omega$ ID=3.5A $RL=57\Omega$ ID=3.5A $RL=57\Omega$ ID=3.5A ID

#### **Avalanche Resistance Test Circuit**







140

160

IT12227

Note on usage: Since the 2SK4101FS is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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